

WEST Search History

DATE: Thursday, August 05, 2004

Hide?	Set Name	Query	Hit Count
	<i>DB=JPAB; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L21	L20 and third gate	23
<input type="checkbox"/>	L20	L19 and insulat\$ adj2 (layer or film)	10531
<input type="checkbox"/>	L19	L18 and surface	20706
<input type="checkbox"/>	L18	gate and surface and substrate	20706
	<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L17	09616072	1
<input type="checkbox"/>	L16	10206982	1
	<i>DB=PGPB; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L15	10206982	1
	<i>DB=USPT; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L14	10206982	0
	<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L13	L12 and eras\$ adj3 gate	40
<input type="checkbox"/>	L12	L11 and memory	179
<input type="checkbox"/>	L11	L10 and third adj2 gate	228
<input type="checkbox"/>	L10	L9 and poly\$	4462
<input type="checkbox"/>	L9	L8 and silicide	17320
<input type="checkbox"/>	L8	L7 and (film or layer)	59469
<input type="checkbox"/>	L7	L6 and (insulat\$ or dielectric\$)	61121
<input type="checkbox"/>	L6	L5 and gate	75815
<input type="checkbox"/>	L5	L3 and well	175469
<input type="checkbox"/>	L4	l3 and well	175469
<input type="checkbox"/>	L3	L1 and substrate	239538
<input type="checkbox"/>	L2	L1 and sbstrate	9
<input type="checkbox"/>	L1	semiconductor	417840

VIET Q. NGUYEN
PRIMARY EXAMINER

END OF SEARCH HISTORY